NPIC/TSSG/RED/SDB-009-70
6 February 1970

MEMORANDUM FOR THE RECORD

SUBJECT: High Precision Stereo Comparator (HPSC) Meeting

1. A meeting of the Stereo Comparator Coordinating Group was called at 1030 on 6 February 1960 in 2N-414J. The following were in attendance:
25X1 IEG/PHD- [redacted] PSG/AID- [redacted] APSD- [redacted] 25X1
25X1 ESD- [redacted] RED- [redacted] 25X1

2. This meeting was called to discuss and make recommendations for the solution of the problem of computer storage capacity for the HPSC. The 4 Option proposal from [redacted] 25X1 was specifically referenced.

25X1 a. [redacted] stated that, while there were some advantages of drum storage, core storage was the most reliable and was the easiest and simplest to use from a programming point of view. He also stated that, while the addition of an 8K core would solve the immediate storage problem, there would be little or nothing left for changes or additional programming requirements.

25X1 b. [redacted] was concerned about any differences between core and drum function from an operators point of view. [redacted] 25X1 stated that, as far as the operator was concerned, there would be no difference.

25X1 c. [redacted] added that, from the maintenance considerations, core memory was more reliable. Drums are mechanical and do wear out, any malfunction causes the loss of the entire drum storage capability.

3. As the result of the storage problem discussions the following recommendations were made:

a. To solve immediate problems, a minimum of 8K core memory needs to be added to the HPSC [redacted] 516 Computer. 25X1

b. To solve near and future storage problems a 16K core memory is recommended.

[redacted] 25X1

Project Monitor
NPIC/TSSG/RED/SDB

Declass Review by NIMA/DOD

GROUP 1
Excluded from automatic downgrading and declassification

Distribution:
Original - Route to TSSG
2 - NPIC/IEG/PHD
2 - NPIC/PSG/AID
1 - NPIC/TSSG/ESD
1 - NPIC/TSSG/APSD
1 - NPIC/TSSG/RED